

Dr. BABASAHEB AMBEDKAR MARATHWADA UNIVERSITY
AURANGABAD.

B.Sc.II Year Practical Examination
Electronics (Paper VII)

Center :

Batch No :

Seat No.

Date :

1. Study of AND, OR, NAND, NOR gates using Ics.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations (Truth Table)	15	
4	Results, conclusion	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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2. Half and full adder using gates.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations (Truth Table)	15	
4	Results, conclusion	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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3. Study of 4-bit parallel adder subtractor using IC7483.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations (Truth Table)	15	
4	Results, conclusion	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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4. Study of asynchronous binary decade UP counter.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations (Truth Table)	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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5. Study of synchronous binary DOWN counter (MOD 16).

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations (Truth Table)	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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6. Study of 6-bit ring counter.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations (Truth Table)	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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7. Study of D/A converter using R-2R ladder network.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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8. Study of DAC using IC0800.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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9. Study of ADC using IC0804.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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10. Write a program to find average of block of data containing N numbers.

Sr.	Head	Max. Marks	Marks
1	Flow chart	10	
2	Program coding	15	
3	Program Execution	15	
4	Results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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11. Write a program to determine whether the number is even or odd. If the number is odd, copy 00H to ML otherwise EEH.

Sr.	Head	Max. Marks	Marks
1	Flow chart	10	
2	Program coding	15	
3	Program Execution	15	
4	Results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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12. Write a program to find factorial of 8-bit number.

Sr.	Head	Max. Marks	Marks
1	Flow chart	10	
2	Program coding	15	
3	Program Execution	15	
4	Results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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13. Write a program to find square root of a 16-bit number.

Sr.	Head	Max. Marks	Marks
1	Flow chart	10	
2	Program coding	15	
3	Program Execution	15	
4	Results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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14. Write a program to find sum of 8 bit numbers in a given array.

Sr.	Head	Max. Marks	Marks
1	Flow chart	10	
2	Program coding	15	
3	Program Execution	15	
4	Results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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Date :

15. Write a program to arrange given array of 8 bit numbers in ascending order.

Sr.	Head	Max. Marks	Marks
1	Flow chart	10	
2	Program coding	15	
3	Program Execution	15	
4	Results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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Date :

16. Write a program to arrange given array of 16 bit numbers in ascending order.

Sr.	Head	Max. Marks	Marks
1	Flow chart	10	
2	Program coding	15	
3	Program Execution	15	
4	Results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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Center :

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Date :

1. Study of SCR:

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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Center :

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Date :

2. Firing of two SCRs using one UJT.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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Date :

3. Study of TRIAC light dimmer.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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Date :

4. Study of resolution and sensitivity of 3 and 1/2 digit DVM using 7107.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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5. Use of PT 100 for measurement of temperature using instrumentation amplifier.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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6. Study of 4-digit frequency meter using IC74926.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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7. Use of semiconductor sensor LM335 for measurement of temperature using Transducer Bridge:

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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8. ON-OFF temperature controller using LM34 /AD590.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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9. Study of Linear regulator using IC723 (Fixed and variable voltage).

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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10. Study of fire/flame security system using thermister.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	20	
4	Results, Conclusion	05	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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11. Study of load cell.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	15	
4	Graph, results	15	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	

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12. Study of inductive proximity switch.

Sr.	Head	Max. Marks	Marks
1	Circuit Diagram	10	
2	Connections	15	
3	Observations	20	
4	Results, conclusion	10	
5	Viva voce	10	
6	Record	10	
7	Project hardware	10	
8	Presentation	15	